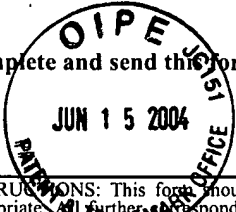


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023720 7590 04/26/2004
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<u>Mary Paul</u>	(Depositor's name)
<u>Mary Paul</u>	(Signature)
<u>June 8, 2004</u>	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/847.622	05/02/2001	Manfred Horstmann	2000.064200	5606

TITLE OF INVENTION: FIELD EFFECT TRANSISTOR WITH REDUCED GATE DELAY AND METHOD OF FABRICATING THE SAME

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	07/26/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
FENTY, JESSE A	2815	257-412000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.

"Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a **Customer Number** is required.

2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1 Williams, Morgan &
 2 Amerson, P.C.
 3 _____

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. Inclusion of assignee data is only appropriate when an assignment has been previously submitted to the USPTO or is being submitted under separate cover. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE **Advanced Micro Devices, Inc.** (B) RESIDENCE: (CITY and STATE OR COUNTRY) **Austin, Texas**

Please check the appropriate assignee category or categories (will not be printed on the patent); individual corporation or other private group entity government

4a. The following fee(s) are enclosed:

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 Publication Fee
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(Authorized Signature) [Signature] (Date) June 8, 2004

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WILLIAMS, MORGAN & AMERSON, P.C.

10333 RICHMOND, STE. 1100, HOUSTON, TEXAS 77042

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WRITER'S DIRECT DIAL:
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FILE: 2000.064200

June 8, 2004

CERTIFICATE OF MAILING 37 C.F.R. § 1.8	
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June 8, 2004 Date	 Signature

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RE: U.S. Patent Application Serial No. 10/847,622
 Entitled: "FIELD EFFECT TRANSISTOR WITH REDUCED GATE DELAY AND METHOD OF FABRICATING THE SAME"
 Inventor(s): Manfred Horstman, Rolf Stephan, Karsten Wieczorek and Stephen Kruegel
 Client Reference: DE0053

Sir:

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2. A return postcard to acknowledge receipt of these materials. Please date stamp and mail this postcard.

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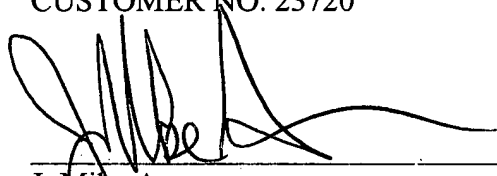
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June 8, 2004
Page 2

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Respectfully submitted,

WILLIAMS, MORGAN & AMERSON, P.C.
CUSTOMER NO. 23720

A handwritten signature in black ink, appearing to read "J. Mike Amerson", is written over a horizontal line.

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ATTORNEY FOR APPLICANTS

JMA/mp

Enclosures

cc: Ms. Samantha Cardona (w/enc.)